



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)		ATTY. DOCKET NO. 043876-0144		SERIAL NO. 10/616,303			
		APPLICANT John MOUSSOURIS, et al.					
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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US					
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
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/DW/		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications and Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (Mar. 1992) (50006DOC018530 - 563)					
↓		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)," IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995) (50006DOC018413 - 529)					
		Gerry Kane et al., "MIPS RISC Architecture," Prentice Hall (1995) (50006DOC018576 - 848)					
/DW/		Undy et al., "A Low-Cost Graphics and Multimedia Workstation Chip Set," IEEE Micro, pp. 10-22 (April 1994) (51056DOC002578 - 590)					
EXAMINER /Daniel Washburn/			DATE CONSIDERED 03/09/2007				

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